

The circuit diagram shows a differential amplifier stage. The input stage consists of a differential pair of transistors, labeled 1 and 2, with gates connected to a common-mode input signal (indicated by a wavy line). The drains of these transistors are connected to a current mirror load, which includes transistors 3 and 4, and resistors 11 and 12. The gates of transistors 3 and 4 are connected to a common-mode feedback signal (indicated by a wavy line). The sources of transistors 3 and 4 are connected to ground. The outputs of the differential pair are connected to a differential-to-single-ended converter, which includes transistors 5 and 6, and resistors 13 and 14. The gates of transistors 5 and 6 are connected to a common-mode input signal (indicated by a wavy line). The sources of transistors 5 and 6 are connected to ground. The drains of transistors 5 and 6 are connected to a common-mode feedback signal (indicated by a wavy line). The output of the differential amplifier is taken from the drain of transistor 6.

SIMPLE LOAD-DRIVING CIRCUIT CAPABLE OF DRIVING PLURAL
LOADS ACCORDING TO GIVEN PRIORITY ORDER

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FIG. 3

